SUPER®

SUPER X6DAL-XG

USER'S MANUAL

Revision 1.0

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Preface

About This Manual

This manual is written for system integrators, PC technicians and knowledgeable PC users. It provides information for the installation and use of the SUPERO X6DAL-XG motherboard. The SUPERO X6DAL-XG supports single or dual Intel® Xeon Nocona™ processors at a 8 00 MHz front side bus. Based upon Intel's NetBurst microarchitecture with EM64T support, the Nocona processor supports IA-32 and IA-64 software and includes features found in the Xeon™ processor such as Hyper Pipelined Technology, which includes a multi-stage pipeline, allowing the processor to operate at much higher core frequencies. Packaged in a 604-pin Flip Chip Micro Pin Grid Array(FC-mPGA4) platform in a Zero Insertion Force(ZIF) socket (mPGA 604), the Nocona Processor (800 MHz) supports Hyper-Threading Technology and is ideal for high performance workstation and server environments with up to two processors on one system bus. Please refer to the motherboard specifications pages on our web site (http://www.supermicro.com/products/motherboard/) for updates on supported processors. This product is intended to be professionally installed.

Manual Organization

Chapter 1 begins with a checklist of what should be included in your mainboard box, describes the features, specifications and performance of the motherboard and provides detailed information about the chipset.

Chapter 2 begins with instructions on handling static-sensitive devices. Read this chapter when you want to install the processor and DIMM memory modules and when mounting the mainboard in the chassis. Also refer to this chapter to connect the floppy and hard disk drives, SCSI drives, the IDE interfaces, the serial ports, the keyboard and mouse, the power supply and various control panel buttons and indicators.

If you encounter any problems, see **Chapter 3**, which describes troubleshooting procedures for the video, the memory and the setup configuration stored in CMOS. For quick reference, a general FAQ [Frequently Asked Questions] section is provided.

Chapter 4 includes an introduction to BIOS and provides detailed information on running the CMOS Setup utility.

Appendix A lists BIOS Error Beep Codes and DS LED POST Codes.

Appendix B provides BIOS POST codes.

Appendix C provides software installation instructions.

Appendix D provides installation instructions on Adaptec's SATA HostRAID Driver based on Marvell's chip.

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Chapter 1
Introduction

1-1 Overview

Checklist

Congratulations on purchasing your computer motherboard from an acknowledged leader in the industry. Supermicro boards are designed with the utmost attention to detail to provide you with the highest standards in quality and performance. Check that the following items have all been included with your motherboard. If anything listed here is damaged or missing, contact your retailer. All included with Retail Box.

One (1) Supermicro Mainboard

One (1) ribbon cable for IDE devices (CBL-036)

One (1) floppy ribbon cable (CBL-022)

One (1) SATA cable (CBL-044)

One (1) CPU mounting plate (SKT-159)

Two (2) CPU retention brackets (SKT-158: pre-installed)

One (1) I/O backpanel shield (CSE-PT2)

One (1) Supermicro CD containing drivers and utilities (CDR_INTC)

One (1) User's/BIOS Manual

Contacting Supermicro

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Figure 1-1. SUPER X6DAL-XG Image

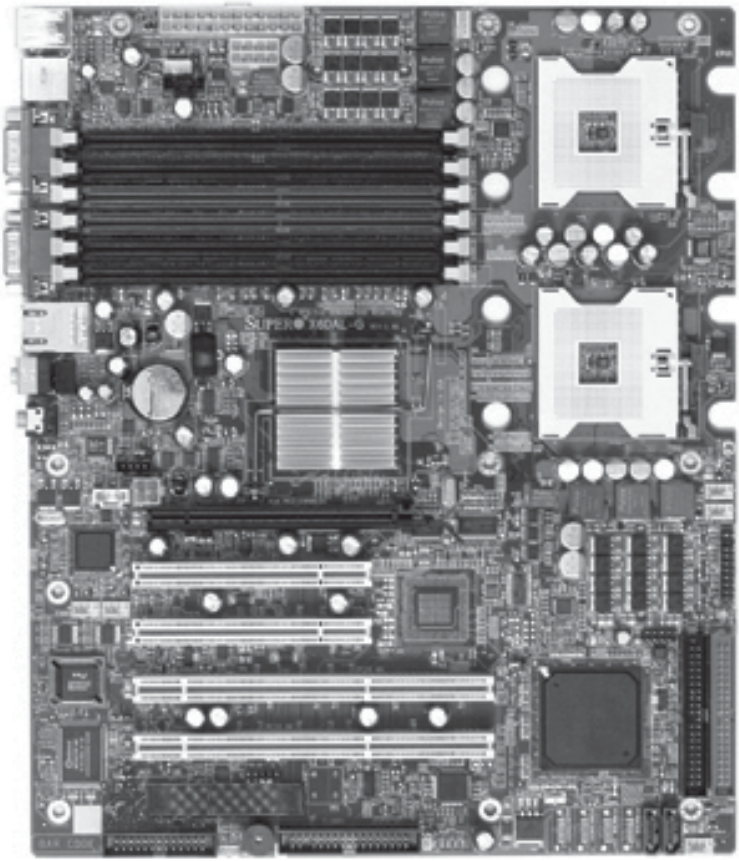
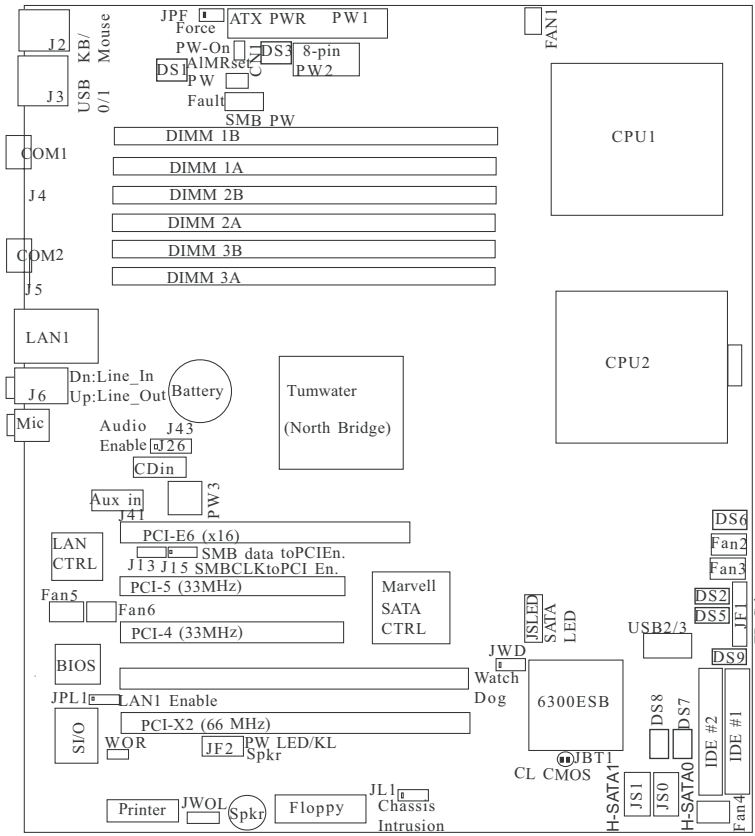


Figure 1-2. SUPER X6DAL-XG Motherboard Layout

(not drawn to scale)



Note:

"■ " indicates the location of pin 1.

X6DAL-XG Quick Reference

Jumper	Description	Default Setting
CN1	Alarm Reset	Open (Disabled)
J13	SMB Data to PCI Enable	Closed (Enabled)
J15	SMB Clock to PCI Enable	Closed (Enabled)
J26	Audio Enable	Closed (Enabled)
JBT1	Clear CMOS	See Chapter 2
JPF	Force Power On Enable	Open (Disabled)
JPL1	LAN1 Enable	Pins 1-2 (Enabled)
JWD	Watch Dog Enable	Pins 2-3 (NMI)

Connector	Description
ATX PWR	Primary 24-pin ATX PWR Connector
PWR2/PWR 3	12V 8-pin CPU PWR/12V 4-pin PWR Connectors
COM1(J4)/COM2 (J5)	COM1/COM2 Serial Port Connectors
DS7, DS8	POST Code LED (*See Appendix A)
FAN #1-#6	CPU/Chassis Fans Headers
DIMM#1A-#3B	Memory (DIMM) Slots# (1A,1B, 2A,2B, 3A,3B)
LAN 1	G-bit Ethernet Port
IDE1, IDE2	IDE1/2 Hard Disk Drive Connectors
J2	Keyboard/Mouse
J6	Down: Line_in, Up: Line_out
J7	Power Fault Header
J24	Floppy Disk Drive Connector
J27	Power System Management
J41	Aux. In
J50	CD in
J61	Microphone
JF1	Front Panel Control (See Chapter 2)
JF2	Speaker, PWR LED, Keylock (See Chapter 2)
JL1	Chassis Intrusion Header
JSLED	SATA LED Header
H-SATA 0/1 (JS0/JS1)	Hance Rapids-Serial ATA Ports 0/1
Parallel	Parallel (printer) Port
WOL(JWOL)	Wake-on-LAN
WOR (JWOR)	Wake-on-Ring Header
USB 0/1	Back Panel Universal Serial Bus Ports
USB 2/3 (J42)	Front Panel Universal Serial Bus Ports

Motherboard Features

CPU

- Single or dual Intel® 604-pin Nocona™ processors (*w/EM64T support) at an 800 MHz front side (system) bus speed.

Memory

- Six 184-pin DIMM sockets supporting 12 GB/24GB Registered ECC DDR 333/266 (PC2700/PC2100) SDRAM

  Notes: 1. Memory size is set via BIOS. 2. Interleaved memory; requires memory modules to be installed in pairs. See Section 2-3 for details.

Chipset

- Intel E7525 (Tumwater) chipset

Expansion Slots

- One PCI-E (x16@4GB/sec)
- Two 64-bit 66MHz PCI-X
- Two 32-bit 33MHz PCI slots

BIOS

- 8 Mb AMI® Flash ROM
- APM 1.2, DMI 2.1, PCI 2.2, ACPI 2.0, Plug and Play (PnP), SMBIOS 2.3

PC Health Monitoring

- Onboard voltage monitors for CPU cores, chipset voltage, 3.3V, +5V, +12V, 3.3V standby, and DIMM voltage.
- Fan status monitor with fan speed control via BIOS
- CPU/chassis temperature monitors
- Environmental temperature monitor and control via Supero Doctor III
- CPU fan auto-off in sleep mode
- CPU slow-down on temperature overheat
- CPU thermal trip support for processor protection, +5V standby alert LED
- Power-up mode control for recovery from AC power loss
- Auto-switching voltage regulator for CPU core
- System overheat LED and control
- Chassis intrusion detection
- System resource alert via Super Doctor III

ACPI Features

- Microsoft OnNow
- Slow blinking LED for suspend state indicator
- Main switch override mechanism

Onboard I/O

- 1 Intel 82541PI Gigabit Ethernet controller
- 2 EIDE Ultra DMA/100 bus master interfaces
- 1 floppy port interface (up to 2.88 MB)
- 1 EPP/ECP Parallel Port
- PS/2 mouse and PS/2 keyboard ports
- Up to four USB 2.0 (Universal Serial Bus) (2 ports-back panel, 2 ports-front panel)
- 2 Intel 6300ESB Serial ATA Ports
- Super I/O
- AC '97 CODEC Audio devices supported
- 2 serial ports

Other

- Internal/external modem ring-on
- Wake-on-Ring (WOR)
- Wake-on-LAN (WOL)
- Console redirection

CD/Diskette Utilities

- BIOS flash upgrade utility and device drivers

Dimensions

- ATX 12" x 10" (304.8 x 254 mm)

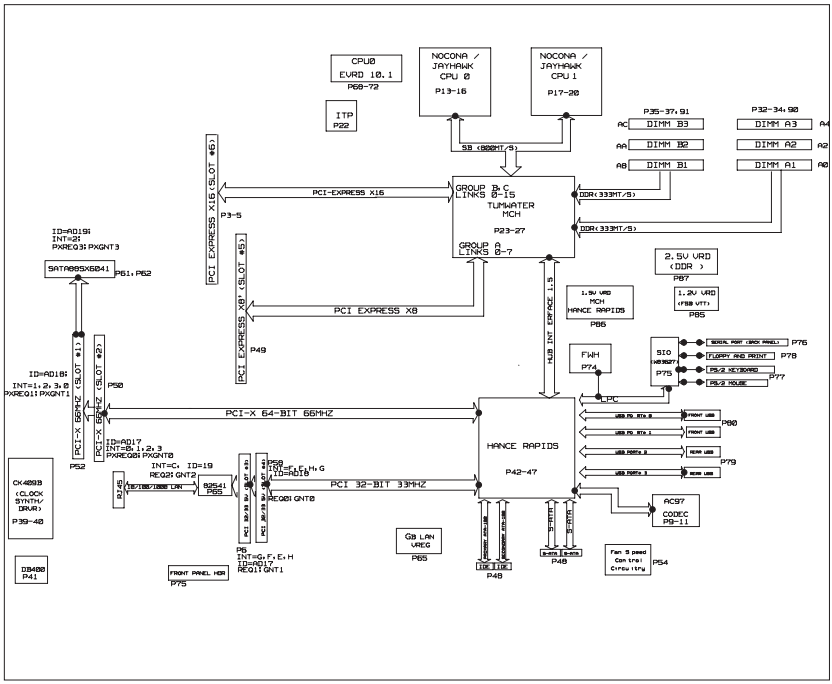


Figure 1-3. Block Diagram of the E7525 Tumwater Chipset

Note: This is a general block diagram. Please see the previous Motherboard Features pages for details on the motherboard.

1-2 Chipset Overview

Built upon the functionality and the capability of the E7525 Tumwater chipset, the X6DAL-XG motherboard provides the performance and feature set required for dual processor-based servers, with configuration options optimized for communications, presentation, storage, computation or database applications. The Intel E7525 Tumwater chipset consists of the following components: the E7525 Tumwater Memory Controller Hub (MCH), the 6300ESB I/O Controller Hub (Hance Rapids ICH), the Intel PCI-X Hub and the PCI-X Hub (PXH).

The E7525 Tumwater MCH supports single or dual Nocona processors with Front Side Bus speeds of up to 800 MHz(*Note). Its memory controller provides direct connection to two channels of registered DDR333 or DDR266 with a marched system bus address and data bandwidths of up to 2.67 GB/s (DDR 333) per channel. The E7525 Tumwater also supports the new PCI Express high speed serial I/O interface for superior I/O bandwidth. These interfaces support connection of the MCH to a variety of other bridges that are compliant with the PCI Express Interface Specification, Rev. 1.0a, such as 82801ER GLAN Adaptor, the Dobson I/O processor, H/W RAID controllers and TCP/IP Off-load engines. The MCH interfaces with the Hance Rapids ICH I/O Controller Hub via HI 1.5 Hub Interface. The PXH provides connection between a PCI Express interface and two independent PCI bus interfaces that can be configured for standard PCI 2.3 protocol, as well as the enhanced high-frequency PCI-X protocol. The PXH can be configured to support for 32- or 64-bit PCI devices running at 33 MHz, 66 MHz, 100 MHz, or 133 MHz.

6300ESB ICH System Features

In addition to providing the I/O subsystem with access to the rest of the system, the 6300ESB ICH I/O Controller Hub integrates many I/O functions.

The 6300ESB ICH I/O Controller Hub integrates: 2-channel Ultra ATA/100 Bus Master IDE Controller, two Serial ATA (SATA) Host Controllers, SMBus 2.0 Controller, LPC/Flash BIOS Interface, PCI-X (66MHz)/PCI-Express (x16 at 4GB/s) Interface, PCI 2.2 Interface and System Management Controller.

(*Notes: The CPU FSB speed is set at 800 MHz by the Manufacturer. Please do not change the CPU FSB setting.)

1-3 Special Features

BIOS Recovery

The BIOS Recovery function allows you to recover your BIOS image file if the BIOS flashing procedure fails (see Section 3-3).

Recovery from AC Power Loss

BIOS provides a setting for you to determine how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off (in which case you must hit the power switch to turn it back on) or for it to automatically return to a power-on state. See the Power Lost Control setting in the Advanced BIOS Setup section (Peripheral Device Configuration) to change this setting. The default setting is Always On.

1-4 PC Health Monitoring

This section describes the PC health monitoring features of the SUPER X6DAL-XG. All have an onboard System Hardware Monitor chip that supports PC health monitoring.

Onboard Voltage Monitors for the CPU Cores, Chipset Voltage, +3.3V, +5V, +12V, -12v, +3.3V Standby, +2.5V (for DRAM), +1.5V Standby, +1.2V

An onboard voltage monitor will scan these voltages continuously. Once a voltage becomes unstable, a warning is given or an error message is sent to the screen (only when SuperDoctorIII is installed). Users can adjust the voltage thresholds in SuperDoctorIII.

Fan Status Monitor with Firmware/Software On/Off Control

The PC health monitor can check the RPM of the cooling fans. The onboard 4-pin CPU and chassis fans are controlled by the power management functions.

Environmental Temperature Control via Supero DoctorIII

The thermal control sensor monitors the CPU temperature in real time and will increase the speed of the thermal control fan whenever the CPU temperature exceeds a user-defined threshold. The overheat circuitry runs independently from the CPU. It can continue to monitor for overheat conditions even when the CPU is in sleep mode. Once it detects that the CPU temperature is too high, it will automatically increase the speed of the thermal control fan to prevent any overheat damage to the CPU. The onboard chassis thermal circuitry can monitor the overall system temperature and alert users when the chassis temperature is too high.

TM2/CPU VRM Overheat

When the CPU reaches 70^0 C and above (Overheat), the CPU will slow down and CPU Voltage will decrease to reduce CPU power consumption and VRM heat dissipation.

When CPU temperature reaches 78^0 C and above (*Default), the system will go into the throttling state. The Overheat LED and Alarm Buzzer will be turned on. The CPU performance will drop 50%. When this happens, you can go to the Health Monitor Setting in the BIOS to reset CPU Overheat Temperature.

CPU Overheat LED and Control

This feature is available when the user enables the CPU overheat warning function in the BIOS. This allows the user to define an overheat temperature. When this temperature is exceeded, the warning LED is triggered and the fans will speed up.

Auto-Switching Voltage Regulator for the CPU Core

The auto-switching voltage regulator for the CPU core can support up to 120W current and auto-sense voltage IDs ranging from .8375V to 1.6V. This will allow the regulator to run cooler and thus make the system more stable.

1-5 ACPI Features

ACPI stands for Advanced Configuration and Power Interface. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a PC system, including its hardware, operating system and application software. This enables the system to automatically turn on and off peripherals such as CD-ROMs, network cards, hard disk drives and printers. This also includes consumer devices connected to the PC such as VCRs, TVs, telephones and stereos.

In addition to enabling operating system-directed power management, ACPI provides a generic system event mechanism for Plug and Play and an operating system-independent interface for configuration control. ACPI leverages the Plug and Play BIOS data structures while providing a processor architecture-independent implementation that is compatible with Windows 2000, Windows NT 5.0, WindowsXP and Server 2003.

Microsoft OnNow

The OnNow design initiative is a comprehensive, system-wide approach to system and device power control. OnNow is a term for a PC that is always on but appears to be off and responds immediately to user or other requests.

Slow Blinking LED for Suspend-State Indicator

When the CPU goes into a suspend state, the chassis power LED will start blinking to indicate that the CPU is in suspend mode. When the user presses any key, the CPU will wake-up and the LED will automatically stop blinking and remain on.

Main Switch Override Mechanism

When an ATX power supply is used, the power button can function as a system suspend button to make the system enter a SoftOff state. The monitor will be suspended and the hard drive will spin down. Depressing the power button again will cause the whole system to wake-up. During the SoftOff state, the ATX power supply provides power to keep the required circuitry in the system alive. In case the system malfunctions and

you want to turn off the power, just depress and hold the power button for 4 seconds. This option can be set in the Power section of the BIOS Setup routine.

External Modem Ring-On (WOR)

Wake-up events can be triggered by a device such as the external modem ringing when the system is in the SoftOff state. Note that external modem ring-on can only be used with an ATX 2.01 (or above) compliant power supply.

1-6 Power Supply

As with all computer products, a stable power source is necessary for proper and reliable operation. It is even more important for processors that have high CPU clock rates.

The SUPER X6DAL-XG accommodates ATX power supplies. Although most power supplies generally meet the specifications required by the CPU, some are inadequate. You should use one that will supply at least 400W of power. In addition, a +12V, 8-pin CPU power supply and a +12V, 4-pin Aux. power supply are also required for high-load configurations. Also your power supply must supply 1.5A for the Ethernet ports.

NOTE: In addition to the 24-pin main power, a 12V 8-pin power connector (PW2) is required to support Intel Xeon CPUs, and a 12V 4-pin power connector (Aux. PWR) is also required for system power consumption. Failure to provide this extra power will result in instability of the CPU after only a few minutes of operation. See Section 2-5 for details on connecting the power supply.

It is strongly recommended that you use a high quality power supply that meets ATX power supply Specification 2.02 or above. It must also be SSI compliant (info at http://www.ssiforum.org/). Additionally, in areas where noisy power transmission is present, you may choose to install a line filter to shield the computer from noise. It is recommended that you also install a power surge protector to help avoid problems caused by power surges.

1-7 Super I/O

The disk drive adapter functions of the Super I/O chip include a floppy disk drive controller that is compatible with industry standard 82077/765, a data separator, write pre-compensation circuitry, decode logic, data rate selection, a clock generator, drive interface control logic and interrupt and DMA logic. The wide range of functions integrated onto the Super I/O greatly reduces the number of components required for interfacing with floppy disk drives. The Super I/O supports 360 K, 720 K, 1.2 M, 1.44 M or 2.88 M disk drives and data transfer rates of 250 Kb/s, 500 Kb/s or 1 Mb/s. It also provides two high-speed, 16550 compatible serial communication ports (UARTs), one of which supports serial infrared communication. Each UART includes a 16-byte send/receive FIFO, a programmable baud rate generator, complete modem control capability and a processor interrupt system. Both UARTs provide legacy speed with baud rate of up to 115.2 Kbps as well as an advanced speed with baud rates of 250 K, 500 K, or 1 Mb/s, which support higher speed modems.

The Super I/O supports one PC-compatible printer port (SPP), Bi-directional Printer Port (BPP) , Enhanced Parallel Port (EPP) or Extended Capabilities Port (ECP).

The Super I/O provides functions that comply with ACPI (Advanced Configuration and Power Interface), which includes support of legacy and ACPI power management through an SMI or SCI function pin. It also features auto power management to reduce power consumption.

The IRQs, DMAs and I/O space resources of the Super I/O can flexibly adjust to meet ISA PnP requirements, which support ACPI and APM (Advanced Power Management).

Chapter 2

Installation

2-1 Static-Sensitive Devices

Electric-Static-Discharge (ESD) can damage electronic components. To prevent damage to your system board, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the board from the antistatic bag.
- Handle the board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.
- Use only the correct type of onboard CMOS battery as specified by the manufacturer. Do not install the onboard battery upside down to avoid possible explosion.

Unpacking

The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the board, make sure the person handling it is static protected.

2-2 PGA Processor and Heatsink Installation

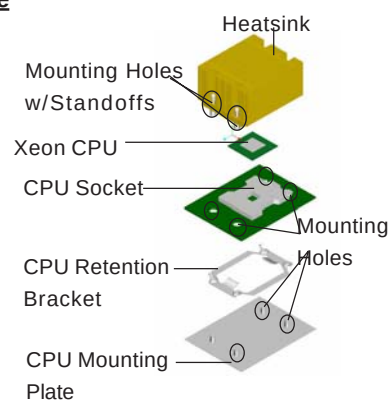
When handling the processor package, avoid placing direct pressure on the label area of the fan. Also, do not place the motherboard on a conductive surface, which can damage the BIOS battery and prevent the system from booting up.

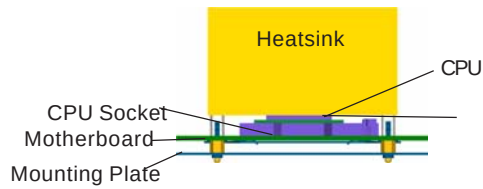
IMPORTANT: Always connect the power cord last and always remove it before adding, removing or changing any hardware components. Make sure that you install the processor into the CPU socket **before** you install the CPU heatsink. Note that for the X6DAL-XG you need to install the Xeon mounting plate under the board.

Installing the CPU Mounting Plate

Note: CPU Retention Brackets are pre-installed.

Install the CPU mounting plate below the retention bracket on the reverse side of the motherboard. See diagram on right.

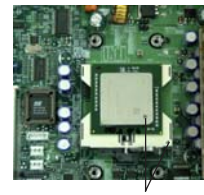




CPU Installation

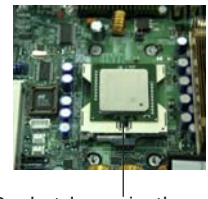
1. Lift the lever on the CPU socket: *lift the lever completely as shown on the picture on the right; otherwise, you will damage the CPU socket when power is applied. (Install CPU1 first.)*

2. Insert the CPU in the socket, making sure that pin 1 of the CPU aligns with pin 1 of the socket (both corners are marked with a triangle). When using only one CPU, install it into CPU socket #1 (Socket #2 is automatically disabled if only one CPU is used).



Pin1

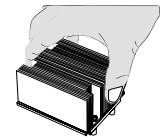
3. Press the lever down until you hear the *click* so you can be sure that the CPU is securely installed in the CPU socket.



Socket lever in the locking Position

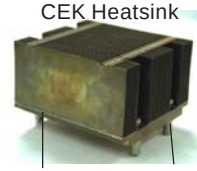
Heatsink Installation

1. Do not apply any thermal grease to the heatsink or the CPU die; the required amount of thermal grease has already been applied.



CEK Heatsink

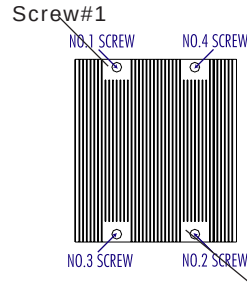
2. Place the heatsink on top of the CPU so that the four mounting holes are aligned with those on the retention mechanism.



Screw#1 Screw#2

3. Screw in two diagonal screws (ie the #1 and the #2 screws) until just snug (-do not fully tighten the screws to avoid possible damage to the CPU.)

4. Finish the installation by fully tightening all four screws.



Note: heatsinks are heavy - please handle them with care.

To Uninstall the Heatsink

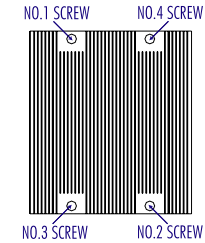
Caution! We do not recommend that the CPU or the heatsink be removed. However, if you do need to uninstall the heatsink, please follow the instructions below to prevent damage to the CPU or the CPU socket.

1. Unscrew and remove the heatsink screws from the motherboard in the sequence as show in the second picture on the right.

2. Hold the heatsink as show in the picture on the right and gently wriggle the heatsink to loosen it from the CPU. (Do not use excessive force when wriggling the heatsink!!)

3. Once the CPU is loosened from the heatsink, remove the heatsink from the CPU socket.

4. Clean the surface of the CPU and the heatsink to get rid of the old thermal grease. Reapply the proper amount of thermal grease on the surface before you re-install the CPU and the heatsink.



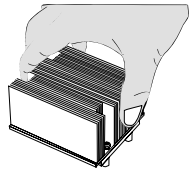
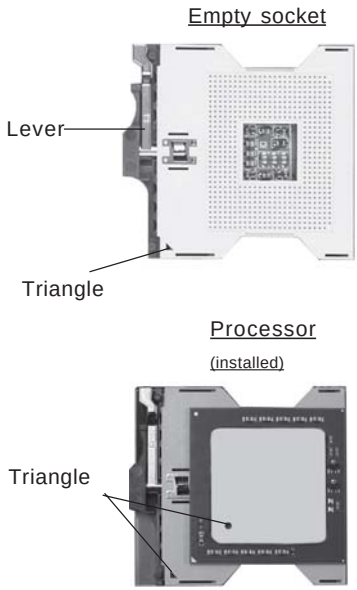


Figure 2-1. PGA604 Socket: Empty and with Processor Installed

Warning! Make sure you lift the lever completely when installing the CPU. If the lever is only partly raised, damage to the socket or CPU may result.



Mounting the Motherboard in the Chassis

All motherboards have standard mounting holes to fit different types of chassis. Make sure the location of all the mounting holes for both the motherboard and the chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are highly recommended because they ground the motherboard to the chassis. Make sure the metal standoffs click in or are screwed in tightly. Then use a screwdriver to secure the motherboard onto the motherboard tray.

2-3 Installing DIMMs

Note: Check the Supermicro web site for recommended memory modules.

CAUTION

Exercise extreme care when installing or removing DIMM modules to prevent any possible damage. Also note that the memory is interleaved to improve performance (see step 1).

DIMM Installation (See Figure 2-2)

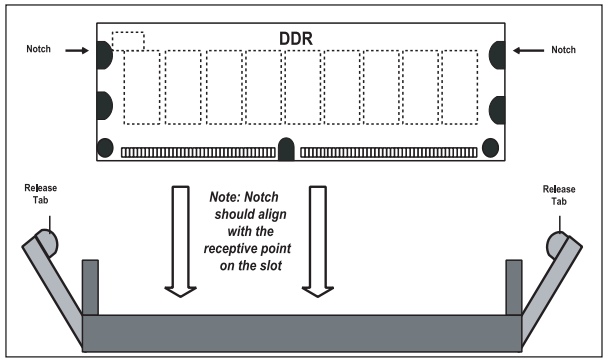
1. Insert the desired number of DIMMs into the memory slots, starting with DIMM #1A. The memory scheme is interleaved so you must install two modules at a time, beginning with DIMM #1A, then DIMM #1B, and so on.
2. Insert each DIMM module vertically into its slot. Pay attention to the notch along the bottom of the module to prevent inserting the DIMM module incorrectly.
3. Gently press down on the DIMM module until it snaps into place in the slot. Repeat for all modules (see step 1 above).

Memory Support

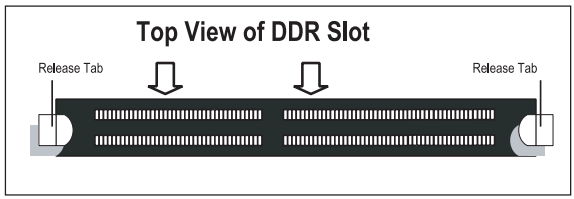
The X6DAL-XG supports up to 12GB/24GB of registered ECC DDR333/266 (PC2700/PC2100) memory. The motherboard was designed to support 4GB (PC2100) modules in each slot, but only the 2GB modules have been tested. When using registered ECC DDR333 (PC2700) memory, installing four pieces of double-banked memory or six pieces of single-banked memory is supported.

Figure 2-2. Installing and Removing DIMMs

To Install:
Insert module vertically and press down until it snaps into place. Pay attention to the alignment notch at the bottom.



To Remove:
Use your thumbs to gently push near the edge of both ends of the module. This should release it from the slot.



2-4 I/OPorts/Control Panel Connectors

The I/O ports are color coded in conformance with the PC 99 specification. See Figure 2-3 below for the locations of the various I/O ports.

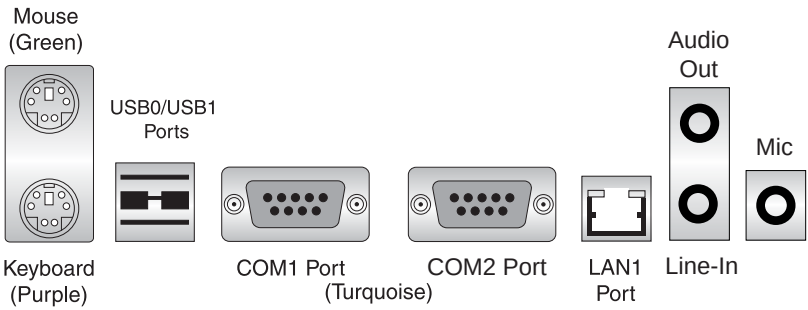
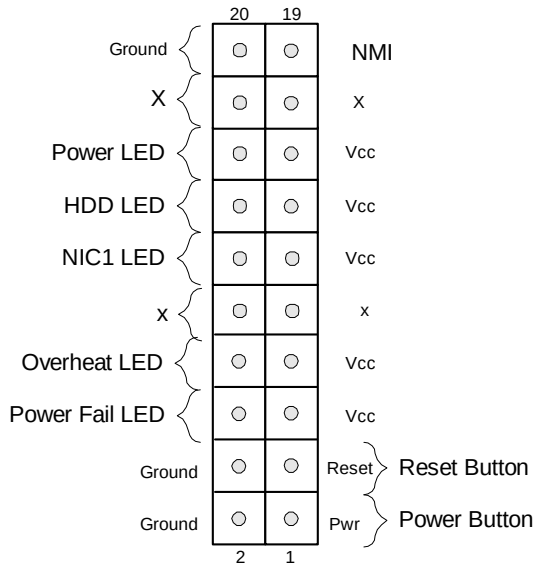


Figure 2-3. I/O Port Locations and Definitions

Front Control Panel

JF1 contains header pins for various buttons and indicators that are normally located on a control panel at the front of the chassis. These connectors are designed specifically for use with Supermicro server chassis. See Figure 2-4 for the descriptions of the various control panel buttons and LED indicators. Refer to the following section for descriptions and pin definitions.

Figure 2-4. JF1 Header Pins



2-5 Connecting Cables

ATX Power Connector

There are a 24-pin main power supply connector(PW1) and a 4-pin 12V PWR connector (PW3) on the board. (Both connections are required.) These power connectors meet the SSI EPS 12V specification. See the table on the right for pin definitions. For CPU PWR (PW2), please refer to the item listed below.

ATX Power Supply 24-pin Connector Pin Definitions (PW1)

Pin Number	Definition	Pin Number	Definition
13	+3.3V	1	+3.3V
14	-12V	2	+3.3V
15	COM	3	COM
16	PS_ON#	4	+5V
17	COM	5	COM
18	COM	6	+5V
19	COM	7	COM
20	Res(NC)	8	PWR_OK
21	+5V	9	5VSB
22	+5V	10	+12V
23	+5V	11	+12V
24	COM	12	+3.3V

Required Connection

+12V 4-pin Connector (PW3)

Pins #	Definition
1 & 2	Ground
3 & 4	+12 V

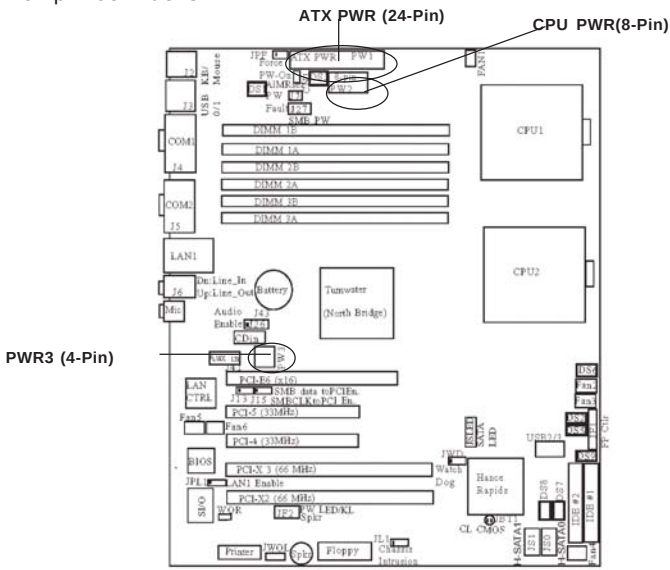
Processor Power Connector

In addition to the power connectors indicated above, the 12v 8-pin Processor connector at PW2 must also be connected to your power supply. See the table on the right for pin definitions.

Required Connection

CPU 8-Pin PWR Connector (PW2)

Pins	Definition
1 thru 4	Ground
5 thru 8	+12v



NMI Button

The non-maskable interrupt button header is located on pins 19 and 20 of JF1. Refer to the table on the right for pin definitions.

NMI Button Pin Definitions (JF1)

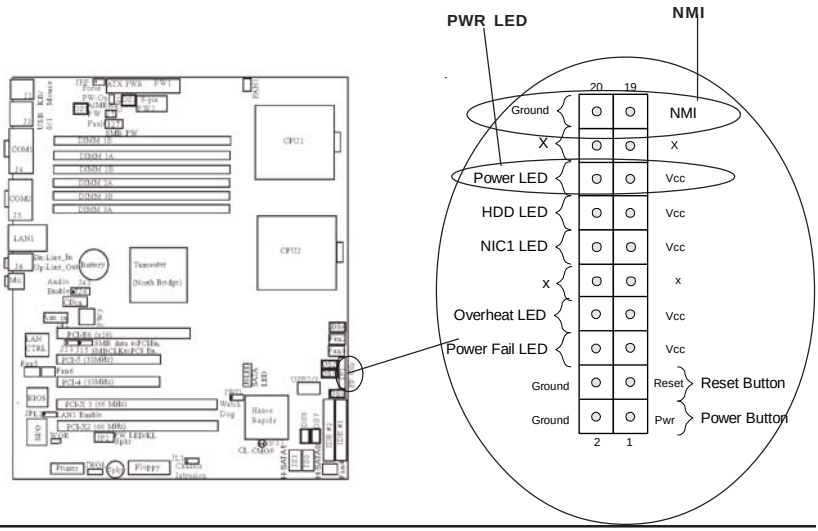
Pin Number	Definition
19	Control
20	Ground

Power LED

The Power LED connection is located on pins 15 and 16 of JF1. Refer to the table on the right for pin definitions.

PWR_LED Pin Definitions (JF1)

Pin Number	Definition
15	Vcc
16	Control



HDD LED

The HDD LED connection is located on pins 13 and 14 of JF1. Attach the hard drive LED cable here to display disk activity (for any hard drives on the system, including SCSI, Serial ATA and IDE). See the table on the right for pin definitions.

HDD LED Pin Definitions (JF1)

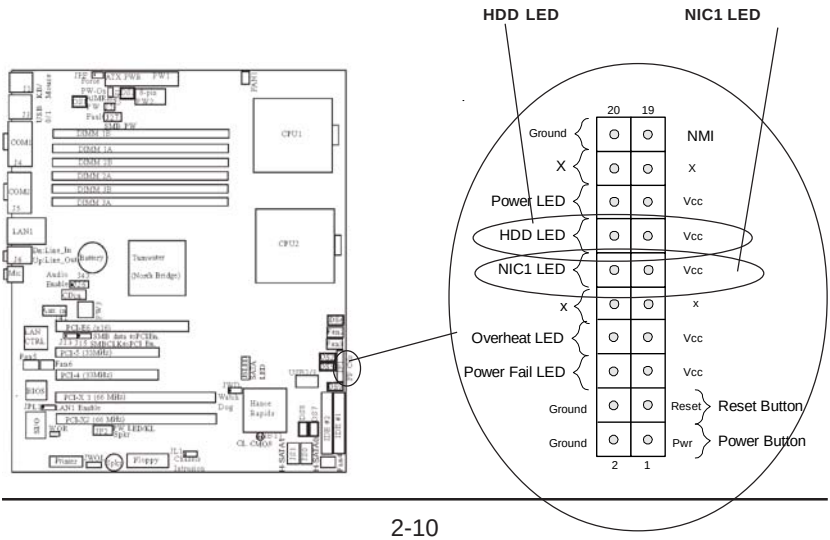
Pin Number	Definition
13	Vcc
14	HD Active

NIC1 LED Indicators

The NIC (Network Interface Controller) LED connections for the GLAN port1 is located on pins 11 and 12 of JF1. Attach the NIC LED cable to display network activity. Refer to the tables on the right for pin definitions.

NIC1 LED Pin Definitions (JF1)

Pin Number	Definition
11	Vcc
12	GND



Overheat/Fan Fail LED

Connect an LED to the OH/Fan Fail connection on pins 7 and 8 of JF1 to provide advanced warning of chassis overheating. Refer to the table on the right for pin definitions.

Overheat/Fan Fail LED Pin Definitions (JF1)

Pin Number	Definition
7	Vcc
8	GND

Overheat/Fan Fail LED

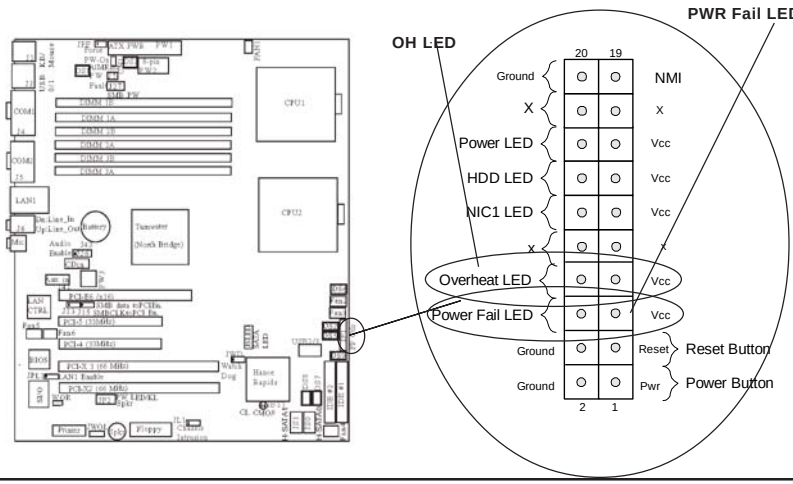
State	Message
Solid	Overheat
Blink	Fan Fail

Power Fail LED

The Power Fail LED connection is located on pins 5 and 6 of JF1. Refer to the table on the right for pin definitions.

Power Fail LED Pin Definitions (JF1)

Pin Number	Definition
5	Vcc
6	GND



Reset Button

The Reset Button connection is located on pins 3 and 4 of JF1. Attach it to the hardware reset switch on the computer case. Refer to the table on the right for pin definitions.

Reset Pin Definitions (JF1)

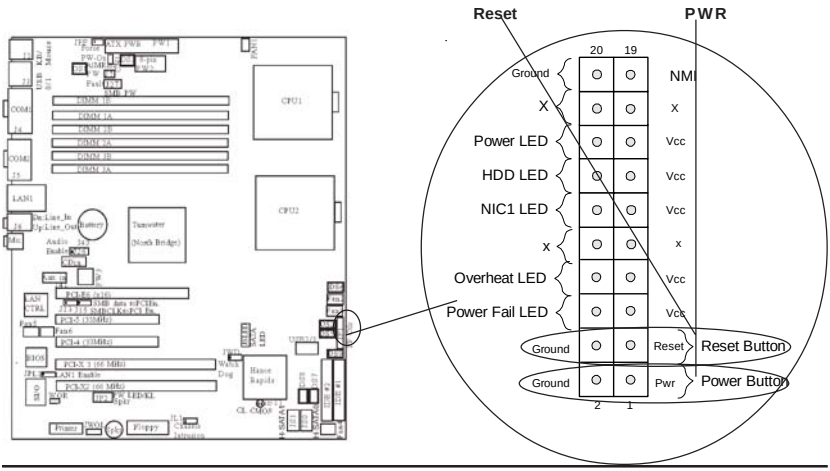
Pin Number	Definition
3	Reset
4	Ground

Power Button

The Power Button connection is located on pins 1 and 2 of JF1. Momentarily contacting both pins will power on/off the system. To turn off the power when set to suspend mode, depress the button for at least 4 seconds. Refer to the table on the right for pin definitions.

Power Button Connector Pin Definitions (JF1)

Pin Number	Definition
1	PW_ON
2	Ground



Chassis Intrusion

A Chassis Intrusion header is located at JL1. Attach the appropriate cable to inform you of a chassis intrusion.

Chassis Intrusion Pin Definitions

Pin Number	Definition
1	Intrusion Input
2	Ground

Universal Serial Bus (USB)

There are two Universal Serial Bus ports(USB 0/1) located on the I/O panel and additional two USB ports(USB 2/3) next to the IDE2 on the motherboard. These two FP USB ports can be used to provide front side chassis access (cables not included). See the tables on the right for pin definitions.

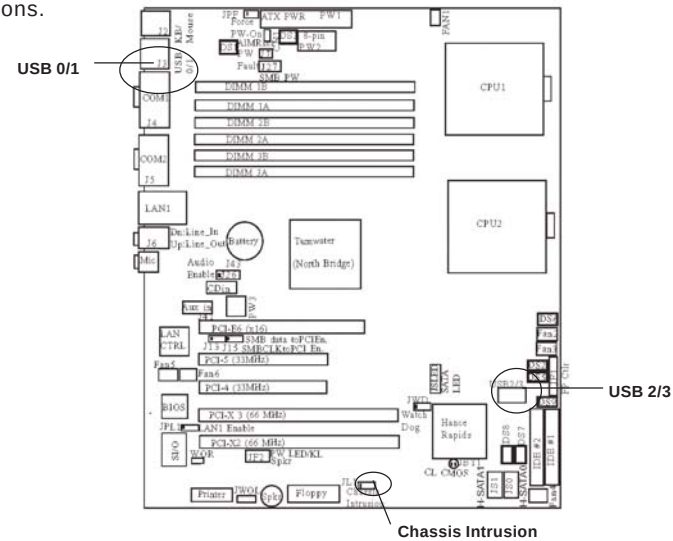
USB Pin Definition

USB O/1 (Back Panel USB)

Pin#	Definition
1	+5V
2	P0-
3	P0+
4	Ground

USB 2/3 (Front Panel USB)

Pin Number	Definition	Pin Number	Definition
1	+5V	2	+5V
3	PO-	4	PO-
5	PO+	6	PO+
7	Ground	8	Ground
		10	Ground



ATX PS/2 Keyboard and PS/2 Mouse Ports

The ATX PS/2 keyboard and the PS/2 mouse are located at J2. See the table on the right for pin definitions. (The mouse port is above the keyboard port. See the table on the right for pin definitions.)

PS/2 Keyboard and Mouse Port Pin Definitions (J2)

Pin Number	Definition
1	Data
2	NC
3	Ground
4	VCC
5	Clock
6	NC

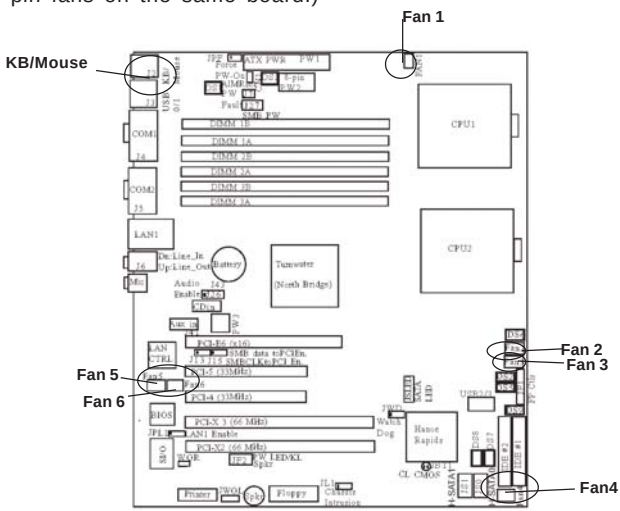
Fan Headers

There are six fan headers (Fan 1 to Fan 6) on the X6DAL-XG). See the table on the right for pin definitions. (*Note: These fan headers are 4-pin fans. Pins#1-#3 of the fan headers are backward compatible with the traditional 3-pin fans.) (*The onboard fan speed is controlled by Thermal Management via BIOS--Hardware Monitor in the Advanced Setting. Note: Default: Disabled, When using Thermal Management setting, please use all 3-pin fans or all 4-pin fans on the motherboard. Please do not use 3-pin fans and 4-pin fans on the same board.)

4-pin Fan Header Pin Definitions (CPU and Chassis Fans)

Pin#	Definition
1	Ground (black)
2	+12V (red)
3	Tachometer
4	PWM_Control

Caution: These fan headers use DC power.



Serial Ports

The COM1 (J4) and COM2 (J5) serial ports are located on the I/O backplane (see Figure 2-3). See the table on the right for pin definitions.

Serial Port Pin Definitions (COM1, COM2)

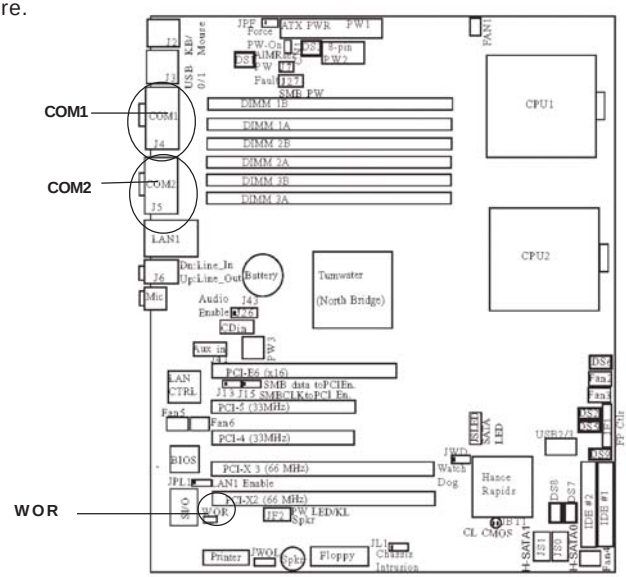
Pin Number	Definition	Pin Number	Definition
1	DCD	6	CTS
2	DSR	7	DTR
3	Serial In	8	RI
4	RTS	9	Ground
5	Serial Out	10	NC

Wake-On-Ring (JWOR)

The Wake-On-Ring header is designated WOR. This function allows your computer to receive and "wake-up" by an incoming call to the modem when in suspend state. See the table on the right for pin definitions. You must have a Wake-On-Ring card and cable to use this feature.

Wake-on-Ring Pin Definitions (WOR)

Pin Number	Definition
1	Ground
2	Wake-up



Wake-On-LAN

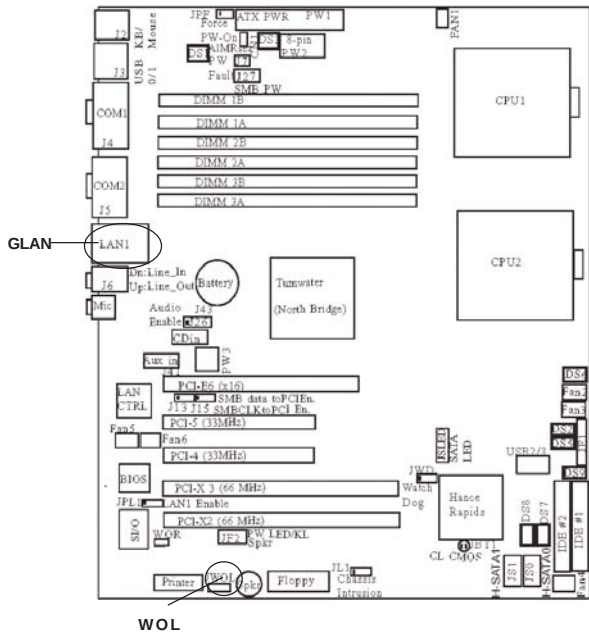
The Wake-On-LAN header(JWOL) is designated WOL on the motherboard. See the table on the right for pin definitions. You must enable the LAN Wake-Up setting in BIOS to use this function. (You must also have a LAN card with a Wake-On-LAN connector and cable to use this feature.)

Wake-On-LAN Pin Definitions (JWOL)

Pin Number	Definition
1	+5V Standby
2	Ground
3	Wake-up

GLAN (Giga-bit Ethernet Port)

A G-bit Ethernet port is located beside the COM2 port on the IO backplane. This port accepts RJ45 type cables.



Power Fault

Connect a cable from your power supply to the Power Fault header (J7) to provide warning of power supply failure. This warning signal is passed through the PWR_LED pin to indicate of a power failure on the chassis. See the table on the right for pin definitions.

Power Fault Pin Definitions

Pin Number	Definition
1	P/S 1 Fail Signal
2	P/S 2 Fail Signal
3	P/S 3 Fail Signal
4	Reset (from MB)

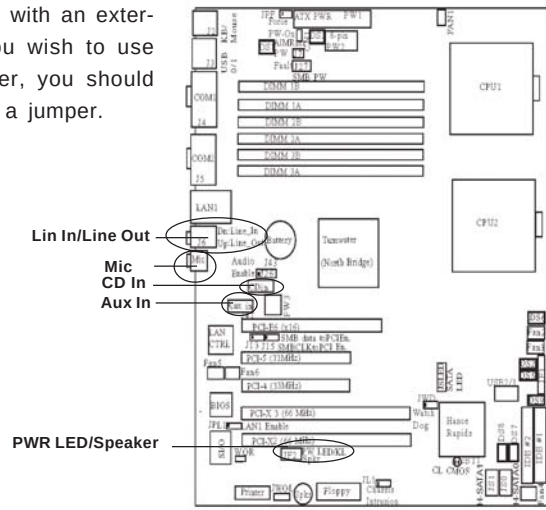
Note: This feature is only available when using redundant Supermicro power supplies.

Speaker/Power LED/ Keylock

On the JF2 header, pins 1/3/5/7 are for the Speaker, and Pins 2/4/6 are for the Power LED and pins 8/9 are for Keylock. See the table on the right for speaker pin definitions. Note: The speaker connector pins are for use with an external speaker. If you wish to use the onboard speaker, you should close pins 5-7 with a jumper.

Speaker Connector Pin Definitions (JF2)

Pin Number	Function	Definition
1	+	Red wire, Speaker data
3	Key	No connection
5		Key
7		Speaker data



SMB Power (I²C) Connector

I²C Connector (J27), located between the Alarm Reset Header and the PWR Fault Header, monitors the status of PWR Supply, Fan and system temperature.

SMB PWR Pin Definitions (J27)

Pin #	Definition
1	Clock
2	SMB Data
3	N/A
4	N/A
5	N/A

CD-In Header

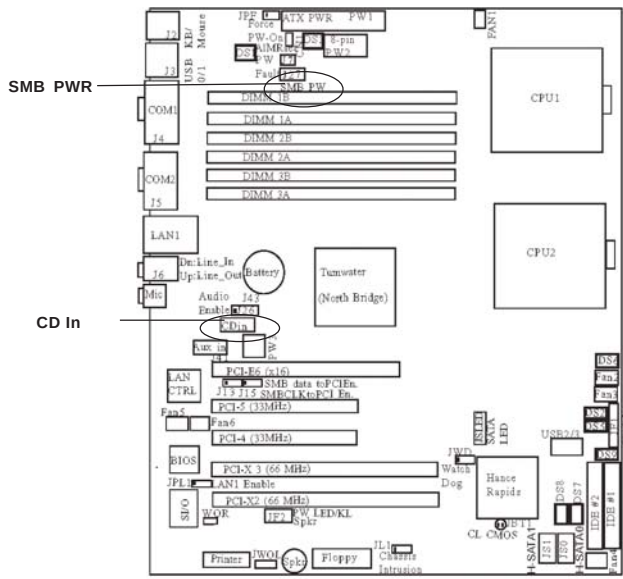
There is a 4-pin CD header on the motherboard. This allows you to use the onboard sound for audio CD playback. Connect the audio cable from your CD drive to the header. See the tables at right for pin definitions.

Audio CD Header Pin Definitions (CD1)

Pin Number	Definition
1	Left Stereo Signal
2	Ground
3	Ground
4	Right Stereo Signal

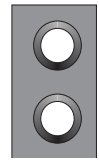
Audio CD Header Pin Definitions (CD2)

Pin Number	Definition
1	Right Stereo Signal
2	Ground
3	Left Stereo Signal
4	Ground



AC 97/AUX In/CD In

AC'97 provides high quality onboard audio. The X6DAL-XG features 6-channel sound for front L&R, rear L&R, center and subwoofer speakers. This feature is activated with the Advanced software (on the CD-ROM included with your motherboard). Sound is then output through the Line In, Line Out and MIC jacks (see at right). Activate AC 97 with the "AC 97 Audio" setting in the Advanced Chipset Features section of BIOS. In addition, there are also Aux In and CD In Connectors on the motherboard.



Blue: **Line In (surround L/R)**

Green: **Line Out (Front L/R)**

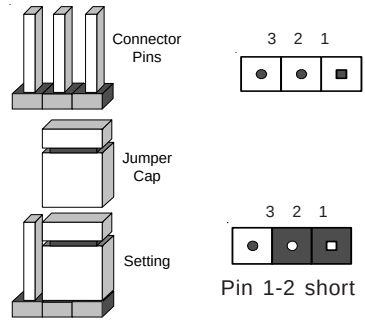
Pink: **MIC In (Center/ subwoofer)**

2-6 Jumper Settings

Explanation of Jumpers

To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the motherboard layout pages for jumper locations.

Note: On two pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.

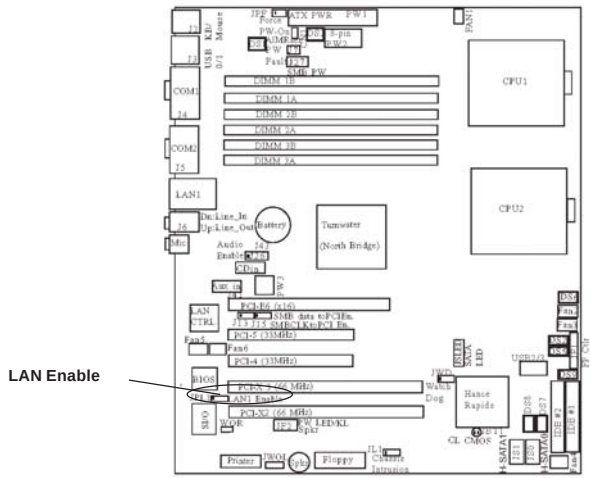


LAN Enable/Disable

JPL1 enables or disable the Giga-bit LAN port on the motherboard. See the table on the right for jumper settings. The default setting is enabled.

GLAN Enable/Disable Jumper Settings (JPL1)

Jumper Position	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled



Alarm Reset

The system will notify you in the event of a power supply failure. This feature assumes that Supermicro redundant power supply units are installed in the chassis. If you only have a single power supply installed, you should disable this (the default setting) with (CN1) to prevent false alarms. See the table on the right for jumper settings.

Alarm Reset Jumper Settings

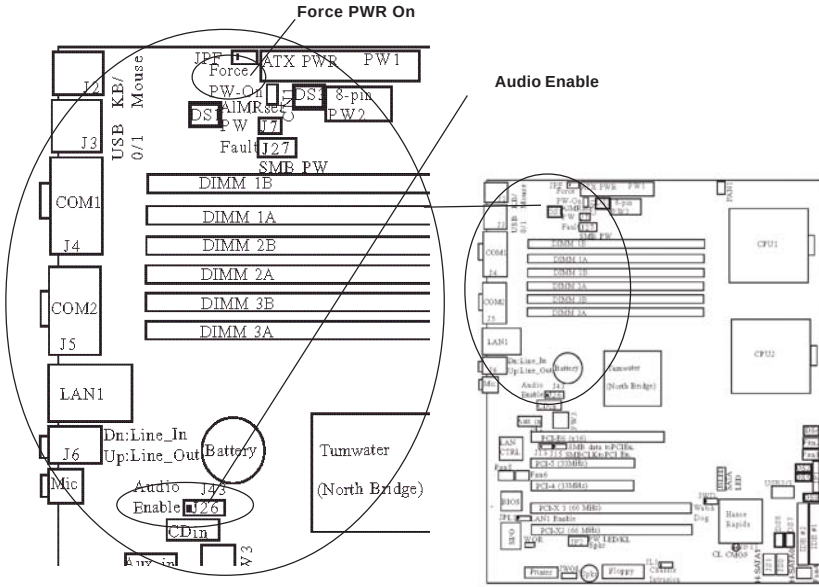
Jumper Position	Definition
Open	Enabled
Closed	Disabled

Force-Power-On Enable/ Disable

Jumper JPF, allows you to enable or disable the function of Force-Power-On. If enabled, the power will always stay on automatically. If this function disabled, the user needs to press the power button to power on the system.

Force Power On (JPF)

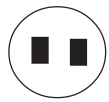
Jumper Position	Definition
Off	Normal
On	Force On



CMOS Clear

JBT1 is used to clear CMOS. Instead of pins, this "jumper" consists of contact pads to prevent the accidental clearing of CMOS. To clear CMOS, use a metal object such as a small screwdriver to touch both pads at the same time to short the connection. Always remove the AC power cord from the system before clearing CMOS. **Note:** For an ATX power supply, you must completely shut down the system, remove the AC power cord and then short JBT1 to clear CMOS.

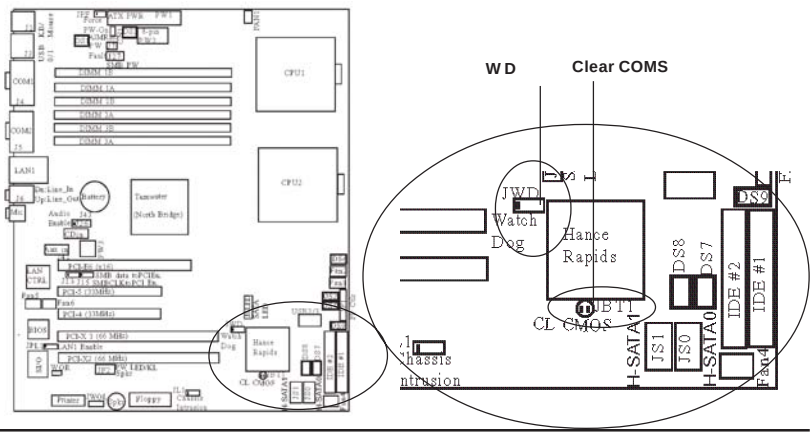
Watch Dog Enable/Disable



JWD enables the Watch Dog function. Watch Dog is a system monitor that can reboot the system when a software application is "hung up". Pins 1-2 will cause WD to reset the system if an application is "hung up". Pins 2-3 will generate a non-maskable interrupt signal for the application that is "hung up". See the table on the right for jumper settings. Watch Dog can also be enabled via BIOS. (*Note, when enabled, the user needs to write his own application software in order to disable the Watch Dog Timer.)

Watch Dog
Jumper Settings (JWD)

Jumper Position	Definition
Pins 1-2	WD to Reset
Pins 2-3	WD to NMI
Open	Disabled



Audio Enable/Disable

J26 enables or disables the Audio Connector on the motherboard. See the table on the right for jumper settings. The default setting is enabled.

Audio Enable/Disable Jumper Settings (J26)

Jumper Position	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

2-7 Onboard Indicators

GLAN LEDs

The Gigabit Ethernet LAN port (located beside the COM Port2) has two LEDs. The yellow LED indicates activity while the other LED may be green, orange or off to indicate the speed of the connection. See the table at right for the functions associated with the second LED.

G-bit LAN Right LED Indicator

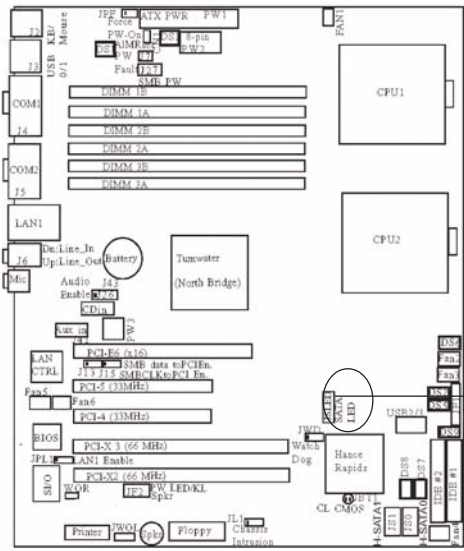
LED Color	Definition
Off	No Connection
Green	100 MHz
Orange	1 GHz

SATA LED Header

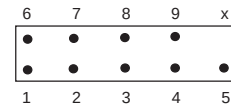
A Serial ATA LED header is located at JSLED. Connect JSLED for indication of SATA drive activity. See the table on the right for pin definitions.

SATA LED Pin Definitions (JSLED)

Pin#	Definition
1-5	No Connection
6	Intel 6300ESB SATA Activity LED
7	Intel 6300ESB SATA Activity LED
8-9	No Connection



JSLED Header: pin 10 is a key.



Onboard LED Indicators (DS1-DS8)

In addition to the LAN LED and SATA Header, there are other LED indicators (DS1-DS3, DS5-DS8) on the X6DAL-XG. See the table on the right for pin definitions. (*Note: Please refer to Appendix A for DS7 and DS8 LED POST Codes.)

On board LED Pin Definitions

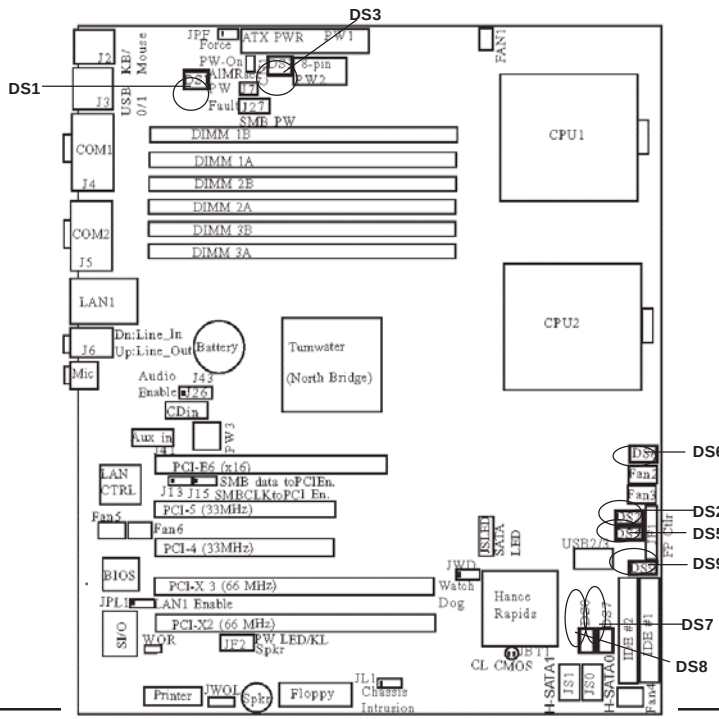
DS#	Definition
DS1	CPU PWR good or CPU +12V PWR Cable must be connected.
DS2	CPU2 VRM Overheat
DS3	CPU1 VRM Overheat
DS5	PWR LED
DS6	CPU Overheat
DS7-8	POST LED

System Alert LED Indicators (DS9)

In addition to the LED indicators listed above, there is a System Alert LED indicator (DS9) on the X6DAL-XG. See the table on the right for pin definitions.

System Alert LED (DS9) Pin Definitions

DS9	Definition
Green	System: On & OK
Yellow	System: Off, PWR Cable Connected
Red	PWR or CPU Failure



2-8 Parallel Port, Floppy/Hard Disk Drive and SCSI Connections

Note the following when connecting the floppy and hard disk drive cables:

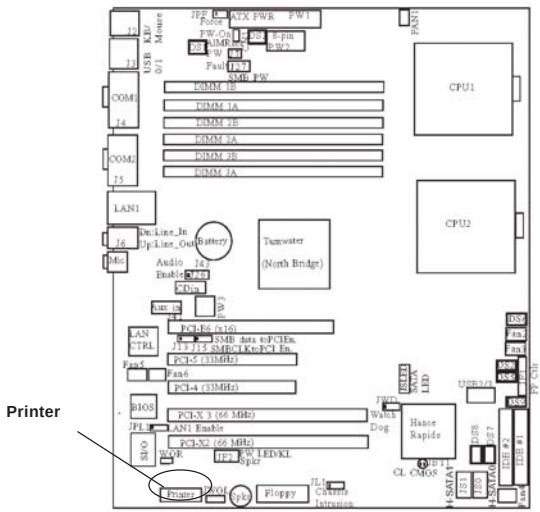
- The floppy disk drive cable has seven twisted wires.
- A red mark on a wire typically designates the location of pin 1.
- A single floppy disk drive ribbon cable has 34 wires and two connectors to provide for two floppy disk drives. The connector with twisted wires always connects to drive A, and the connector that does not have twisted wires always connects to drive B.

Parallel (Printer) Port Connector

There is a parallel (printer) port is located on the motherboard. See the table on the right for pin definitions. (*Note: This is a header. You will need to have a Parallel Port cable: CBL_081 to use this connector.)

Parallel (Printer) Port Pin Definitions

Pin Number	Function	Pin Number	Function
1	Strobe-	2	Auto Feed-
3	Data Bit 0	4	Error-
5	Data Bit 1	6	Init-
7	Data Bit 2	8	SLCT IN-
9	Data Bit 3	10	GND
11	Data Bit 4	12	GND
13	Data Bit 5	14	GND
15	Data Bit 6	16	GND
17	Data Bit 7	18	GND
19	ACK	20	GND
21	BUSY	22	GND
23	PE	24	GND
25	SLCT	26	NC

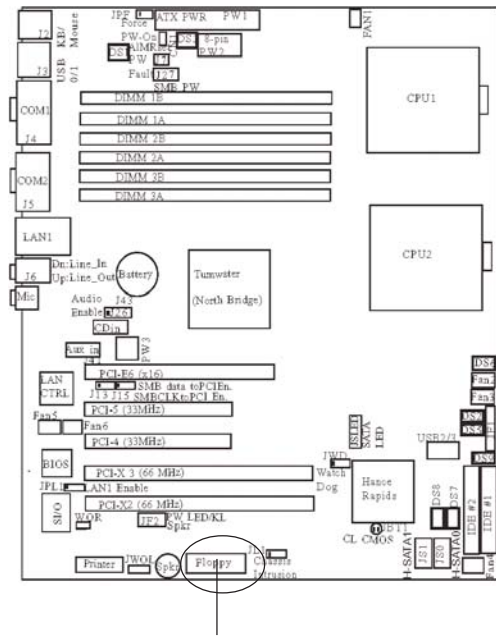


Floppy Connector

The floppy connector is located on J24. See the table below for pin definitions.

Floppy Connector Pin Definitions (J24)

Pin Number	Function	Pin Number	Function
1	GND	2	FDHDIN
3	GND	4	Reserved
5	Key	6	FDEDIN
7	GND	8	Index-
9	GND	10	Motor Enable
11	GND	12	Drive Select B-
13	GND	14	Drive Select A-
15	GND	16	Motor Enable
17	GND	18	DIR-
19	GND	20	STEP-
21	GND	22	Write Data-
23	GND	24	Write Gate-
25	GND	26	Track 00-
27	GND	28	Write Protect-
29	GND	30	Read Data-
31	GND	32	Side 1 Select-
33	GND	34	Diskette

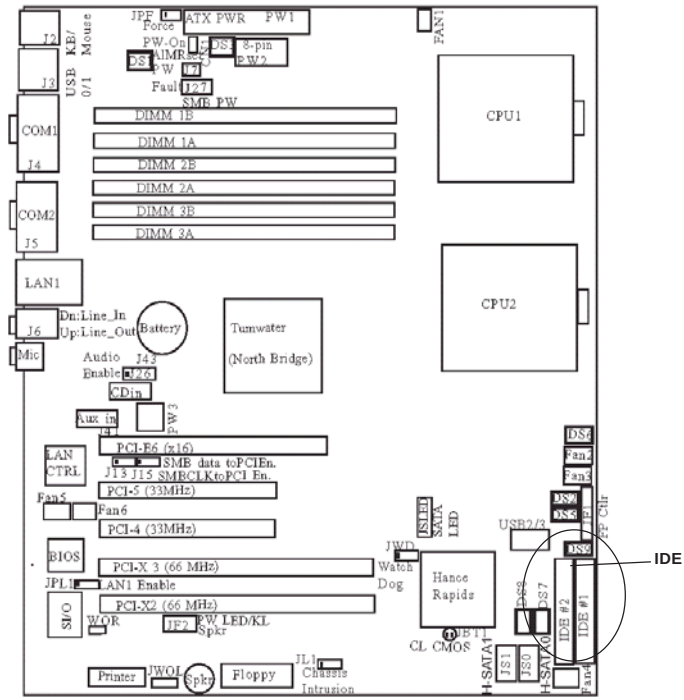


IDE Connectors

The IDE Connectors are located on J44 (IDE1) and J38 (IDE 2), You do not need to configure jumpers for these connectors. See the table on the right for pin definitions.

IDE Connector Pin Definitions (J44, J38)

Pin Number	Function	Pin Number	Function
1	Reset IDE	2	GND
3	Host Data 7	4	Host Data 8
5	Host Data 6	6	Host Data 9
7	Host Data 5	8	Host Data 10
9	Host Data 4	10	Host Data 11
11	Host Data 3	12	Host Data 12
13	Host Data 2	14	Host Data 13
15	Host Data 1	16	Host Data 14
17	Host Data 0	18	Host Data 15
19	GND	20	Key
21	DRQ3	22	GND
23	I/O Write-	24	GND
25	I/O Read-	26	GND
27	IOCHRDY	28	BALE
29	DACK3-	30	GND
31	IRQ14	32	IOCS16-
33	Addr 1	34	GND
35	Addr 0	36	Addr 2
37	Chip Select 0	38	Chip Select 1-
39	Activity	40	GND



Chapter 3

Troubleshooting

3-1 Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the 'Technical Support Procedures' and/or 'Returning Merchandise for Service' section(s) in this chapter.

Note: Always disconnect the power cord before adding, changing or installing any hardware components.

Before Power On

1. Make sure no short circuits exist between the motherboard and chassis.
2. Disconnect all ribbon/wire cables from the motherboard, including those for the keyboard and mouse.
3. Remove all add-on cards.
4. Install one CPU in socket#1 (making sure it is fully seated) and connect the chassis speaker and the power LED to the motherboard. (Check all jumper settings as well.)
5. Use only the correct type of onboard CMOS battery as recommended by the Manufacturer. Do not install the onboard battery upside down to avoid possible explosion.

No Power

1. Make sure no short circuits exist between the motherboard and the chassis.
2. Verify that all jumpers are set to their default positions.
3. Check that the 115V/230V switch on the power supply is properly set.
4. Turn the power switch on and off to test the system.
5. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

No Video

1. If the power is on but you have no video, remove all the add-on cards and cables.
2. Use the speaker to determine if any beep codes exist. Refer to the Appendix for details on beep codes.

NOTE

If you are a system integrator, VAR or OEM, a POST diagnostics card is recommended. For I/O port 80h codes, refer to App. B.

Memory Errors

1. Make sure the DIMM modules are properly and fully installed.
2. Determine if different speeds of DIMMs have been installed and verify that the BIOS setup is configured for the fastest speed of RAM used. It is recommended to use the same RAM speed for all DIMMs in the system.
3. Make sure you are using the correct type of Registered, ECC DDR333/266 (PC2700/PC2100) SDRAM (*recommended by the manufacturer.)
4. Check for bad DIMM modules or slots by swapping a single module between two slots and noting the results.
5. Make sure all memory modules are fully seated in their slots. As an interleaved memory scheme is used, you must install two modules at a time, beginning with DIMM #1A, then DIMM #1B, and so on (see Section 2-3).
6. Check the position of the 115V/230V switch on the power supply.

Losing the System's Setup Configuration

1. Ensure that you are using a high quality power supply. A poor quality power supply may cause the system to lose the CMOS setup information. Refer to Section 1-6 for details on recommended power supplies.
2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.
3. If the above steps do not fix the Setup Configuration problem, contact your vendor for repairs.

3-2 Technical Support Procedures

Before contacting Technical Support, please take the following steps. Also, note that as a motherboard manufacturer, Super Micro does not sell directly to end-users, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problem(s) with the specific system configuration that was sold to you.

1. Please go through the 'Troubleshooting Procedures' and 'Frequently Asked Question' (FAQ) sections in this chapter or see the FAQs on our web site (http://www.supermicro.com/support/faqs/) before contacting Technical Support.
2. BIOS upgrades can be downloaded from our web site at (http://www.supermicro.com/support/bios/).

Note: Not all BIOS can be flashed depending on the modifications to the boot block code.

3. If you still cannot resolve the problem, include the following information when contacting Super Micro for technical support:

- •Motherboard model and PCB revision number
- •BIOS release date/version (this can be seen on the initial display when your system first boots up)
- •System configuration

An example of a Technical Support form is on our web site at (http://www.supermicro.com/support/contact.cfm).

4. Distributors: For immediate assistance, please have your account number ready when placing a call to our technical support department. We can be reached by e-mail at support@supermicro.com, by phone at: (408) 503-8000, option 2, or by fax at (408)503-8019.

3-3 Frequently Asked Questions

Question: What are the various types of memory that my motherboard can support?

Answer: The X6DAL-XG has six 184-pin DIMM slots that support registered ECC DDR333/266 (PC2700/2100) SDRAM modules. It is strongly recommended that you do not mix memory modules of different speeds and sizes.

Question: How do I update my BIOS?

Answer: It is recommended that you **do not** upgrade your BIOS if you are experiencing no problems with your system. Updated BIOS files are located on our web site at http://www.supermicro.com. Please check our BIOS warning message and the information on how to update your BIOS on our web site. Also, check the current BIOS revision and make sure it is newer than your BIOS before downloading.

Question: What's on the CD that came with my motherboard?

Answer: The supplied compact disc has quite a few drivers and programs that will greatly enhance your system. We recommend that you review the CD and install the applications you need. Applications on the CD include chipset drivers for Windows and security and audio drivers. Note: The CD is a bootable disc and can be used to create driver diskettes.

3-4 Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton, and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alternation, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.

Chapter 4

AMIBIOS

4-1 Introduction

This chapter describes the AMIBIOS for the X6DAL-XG. The AMI ROM BIOS is stored in a Flash EEPROM and can be easily upgraded using a floppy disk-based program. This chapter describes the basic navigation of the AMI BIOS Setup Utility setup screens.

Starting the BIOS Setup Utility

To enter the AMI BIOS Setup Utility screens, hit the <Delete> key while the system is booting-up.

(*Note: In most cases, the <Delete> key is used to invoke the AMI BIOS setup screen. There are a few cases when other keys are used, such as <F1>, <F2>, and so on.)

Each main BIOS menu option is described in this user's guide. The Main BIOS setup menu screen has two main frames. The left frame displays all the options that can be configured. "Grayed-out" options cannot be configured. Options in blue can be configured by the user. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it.

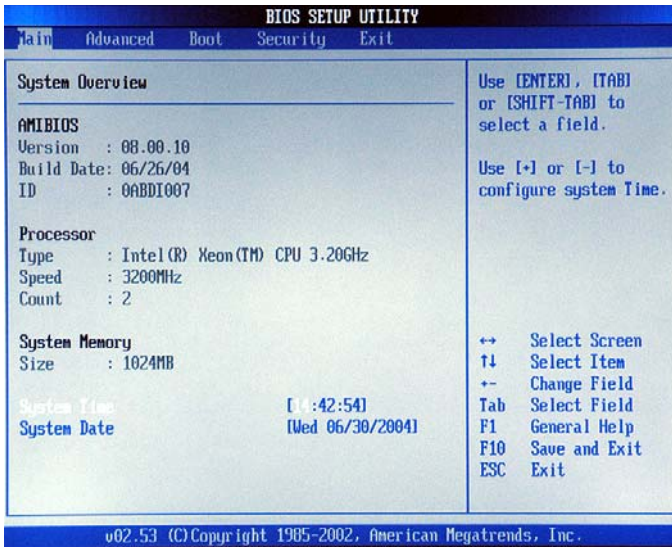
(*Note: The AMI BIOS has default text messages built in. Supermicro retains the option to include, omit, or change any of these text messages.)

The AMI BIOS setup/utility uses a key-based navigation system called hot keys. Most of the AMI BIOS setup utility hot keys can be used at any time during the setup navigation process. These keys include <F1>, <F10>, <Enter>, <ESC>, <Arrow> keys, and so on.

(*Note: Options printed in **Bold** are default settings.)

4-2 Main Setup

When you first enter the AMI BIOS Setup Utility, you will enter the Main setup screen. You can always return to the Main setup screen by selecting the ***Main*** tab on the top of the screen. The Main BIOS Setup screen is shown below.



When you select the Main Setup, the following items will be automatically displayed:

System Overview: The following BIOS information will be displayed:

AMI BIOS
Version
Built Date
ID

Processors

When you select this option, the AMI BIOS will automatically display the status of processors as shown in the screen below:

Type
Speed
Count